

Applicant: Takashi Saida et al.

Confirmation No.: 5371

Serial No.: 10/587,446

Att'y Docket No.: 14321.89

Filing Date: July 16, 2008

Art Unit: 2883

For: PLANAR LIGHTWAVE CIRCUIT, DESIGN METHOD FOR WAVE PROPAGATION  
CIRCUIT, AND COMPUTER PROGRAM

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SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
_____ 1	5,627,928	7/12/94	Thomason

Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>	<u>Translation</u>
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Other Documents

(including author, title, pertinent pages, etc.)

Examiner  
Initial\*

\_\_\_\_\_ 2 Office Action dated September 11, 2009 from related Japanese application No. 200810130022.4.

Other Documents

(including author, title, pertinent pages, etc.)

Examiner  
Initial\***References Cited by Applicants**

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The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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